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Bib Data Sheet

CONFIRMATION NO. 5634

SERIAL NUMBER 10/716,876	FILING DATE 11/20/2003  RULE	CLASS 365	GROUP ART UNIT 2827	ATTORNEY DOCKET NO. 61282-045
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APPLICANTS

Noriko Mizukoshi, Tokyo, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* *NO* \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *NO* \*\*\*\*\*

JAPAN P.2002-336566 11/20/2002  
 JAPAN P.2003-372027 10/31/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 02/19/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 4	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 3
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Verified and Acknowledged *Phan B. Tran* Examiner's Signature *TP* Initials

ADDRESS  
 MCDERMOTT, WILL & EMERY  
 600 13th Street, N.W.  
 Washington, DC  
 20005-3096

TITLE  
 Memory trouble relief circuit

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